

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (canceled)
2. (canceled)
3. (canceled)
4. (currently amended) An analog-to-digital converter system, comprising:
a passive delta sigma converter stage comprising:
a first filter receiving a converter stage analog input and a first analog feedback
signal, the first filter being free of switching components and providing a first filtered
analog signal according to the converter stage analog input and according to the first
analog feedback signal,
a quantizer coupled with the first filter, the quantizer providing a quantized output
according to the first filtered analog signal, and
a first digital-to-analog converter coupled with the first filter and with the
quantizer, the first digital-to-analog converter providing the first analog feedback signal
according to the quantized output; and
an active gain stage coupled with the passive delta sigma converter stage, the active
gain stage receiving a system analog input and providing the converter stage analog input
according to the system analog input and the quantized output, wherein the active gain stage
comprises:
a second filter receiving the system analog input and a second analog feedback
signal, the second filter being free of switching components;

a second digital-to-analog converter coupled with the second filter and with the quantizer, the second digital-to-analog converter providing the second analog feedback signal according to the quantized output; and

an amplifier coupled with the second filter;

wherein the amplifier and the second filter provide the converter stage analog input according to a difference between the system analog input and the second analog feedback signal scaled by a gain factor ~~The system of claim 2~~, wherein the gain factor is about 25 or more.

5. (previously presented) The system of claim 4, wherein the gain factor is about 100.

6. (currently amended) The system of claim ~~[[2]]~~ 4, wherein the second filter is a second order low pass filter.

7. (currently amended) An analog-to-digital converter system, comprising:
a passive delta sigma converter stage comprising:

a first filter receiving a converter stage analog input and a first analog feedback signal, the first filter being free of switching components and providing a first filtered analog signal according to the converter stage analog input and according to the first analog feedback signal,

a quantizer coupled with the first filter, the quantizer providing a quantized output according to the first filtered analog signal, and

a first digital-to-analog converter coupled with the first filter and with the quantizer, the first digital-to-analog converter providing the first analog feedback signal according to the quantized output; and

an active gain stage coupled with the passive delta sigma converter stage, the active gain stage receiving a system analog input and providing the converter stage analog input

according to the system analog input and the quantized output, wherein the active gain stage comprises:

a second filter receiving the system analog input and a second analog feedback signal, the second filter being free of switching components;

a second digital-to-analog converter coupled with the second filter and with the quantizer, the second digital-to-analog converter providing the second analog feedback signal according to the quantized output; and

an amplifier coupled with the second filter;

wherein the amplifier and the second filter provide the converter stage analog input according to a difference between the system analog input and the second analog feedback signal scaled by a gain factor, wherein the second filter is a second order low pass filter and
The system of claim 6, wherein the first filter is a second order low pass filter.

8. (previously presented) The system of claim 7, wherein poles of the second filter are substantially matched with poles of the first filter.

9. (currently amended) An analog-to-digital converter system, comprising:
a passive delta sigma converter stage comprising:

a first filter receiving a converter stage analog input and a first analog feedback signal, the first filter being free of switching components and providing a first filtered analog signal according to the converter stage analog input and according to the first analog feedback signal,

a quantizer coupled with the first filter, the quantizer providing a quantized output according to the first filtered analog signal, and

a first digital-to-analog converter coupled with the first filter and with the quantizer, the first digital-to-analog converter providing the first analog feedback signal according to the quantized output; and

an active gain stage coupled with the passive delta sigma converter stage, the active gain stage receiving a system analog input and providing the converter stage analog input

according to the system analog input and the quantized output, wherein the active gain stage comprises:

a second filter receiving the system analog input and a second analog feedback signal, the second filter being free of switching components;

a second digital-to-analog converter coupled with the second filter and with the quantizer, the second digital-to-analog converter providing the second analog feedback signal according to the quantized output; and

an amplifier coupled with the second filter;

wherein the amplifier and the second filter provide the converter stage analog input according to a difference between the system analog input and the second analog feedback signal scaled by a gain factor ~~The system of claim 2~~, wherein the first and second digital-to-analog converters individually comprise at least one switched capacitor circuit.

10. (currently amended) The system of claim ~~[[1]]~~ 9, wherein the active gain stage provides the converter stage analog input according to a difference between the system analog input and the quantized output scaled by a gain factor.

11. (previously presented) The system of claim 10, wherein the gain factor is about 25 or more.

12. (previously presented) The system of claim 11, wherein the gain factor is about 100.

13. (currently amended) The system of claim ~~[[1]]~~ 9, wherein the first filter is a second order low pass filter.

14. (currently amended) The system of claim ~~[[1]]~~ 7, wherein the first digital-to-analog converter comprises at least one switched capacitor circuit.

15. (canceled)

16. (currently amended) A data converter system, comprising:
a passive circuit comprising:

a quantizer in a forward signal path, the quantizer providing a quantized output
representative of a quantizer input signal, and

a passive filter coupled with the quantizer in the forward signal path, the passive
filter being free of switching components and providing the quantizer input signal
according to a converter input and a first feedback signal;

an active circuit coupled with the passive circuit, the active circuit comprising an active
filter in the forward signal path, the active filter being free of switching components and
providing the converter input according to a difference between a system analog input and a
second feedback signal and according to a gain factor; and

a feedback circuit in a feedback signal path, the feedback circuit being coupled with the
quantizer, the passive filter, and the active filter, the feedback circuit providing the first and
second feedback signals according to the quantized output ~~The system of claim 15, wherein~~
the passive and active filters are second order low pass filters.

17. (previously presented) The system of claim 16, wherein poles of the active filter
are substantially matched with poles of the passive filter.

18. (currently amended) A data converter system, comprising:
a passive circuit comprising:

a quantizer in a forward signal path, the quantizer providing a quantized output
representative of a quantizer input signal, and

a passive filter coupled with the quantizer in the forward signal path, the passive
filter being free of switching components and providing the quantizer input signal
according to a converter input and a first feedback signal;

an active circuit coupled with the passive circuit, the active circuit comprising an active filter in the forward signal path, the active filter being free of switching components and providing the converter input according to a difference between a system analog input and a second feedback signal and according to a gain factor; and

a feedback circuit in a feedback signal path, the feedback circuit being coupled with the quantizer, the passive filter, and the active filter, the feedback circuit providing the first and second feedback signals according to the quantized output The system of claim 15, wherein poles of the active filter are substantially matched with poles of the passive filter.

19. (currently amended) A data converter system, comprising:
a passive circuit comprising:

a quantizer in a forward signal path, the quantizer providing a quantized output representative of a quantizer input signal, and

a passive filter coupled with the quantizer in the forward signal path, the passive filter being free of switching components and providing the quantizer input signal according to a converter input and a first feedback signal;

an active circuit coupled with the passive circuit, the active circuit comprising an active filter in the forward signal path, the active filter being free of switching components and providing the converter input according to a difference between a system analog input and a second feedback signal and according to a gain factor; and

a feedback circuit in a feedback signal path, the feedback circuit being coupled with the quantizer, the passive filter, and the active filter, the feedback circuit providing the first and second feedback signals according to the quantized output The system of claim 15, wherein the gain factor is about 25 or more.

20. (previously presented) The system of claim 19, wherein the gain factor is about 100.

21. (currently amended) The system of claim ~~[[15]]~~ 16, wherein the feedback circuit comprises at least one digital-to-analog converter providing one or more of the first and second feedback signals according to the quantized output.

22. (previously presented) A fourth order delta sigma analog-to-digital converter, comprising:
a passive delta sigma modulator comprising a passive filter, a quantizer, and a digital-to-analog converter in a first feedback loop; and
an amplifier in a second feedback loop around the passive delta-sigma modulator.

23. (previously presented) The converter of claim 22, further comprising a second filter in the second feedback loop.

24. (previously presented) The converter of claim 23, wherein the passive filter and the second filter are second order low pass filters.

25. (previously presented) The converter of claim 23, wherein poles of the passive filter are substantially matched with poles of the second filter.

26. (previously presented) The converter of claim 22, wherein a gain factor of the amplifier is about 25 or more.

27. (previously presented) The converter of claim 22, wherein a gain factor of the amplifier is about 100.